

In re the Application of: Hiroshi HASHIMOTO et al.

Group Art Unit: 2814

Application Number: 10/083,533

Examiner: Thao X. Le

Filed: February 27, 2002

Confirmation Number: 6400

For:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A

SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD

CHARACTERISTIC

Attorney Docket Number:

020244

Customer Number:

38834

August 28, 2006

AMENDMENT UNDER 37 C.F.R. §1.114

Mail Stop RCE Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

This Amendment is being filed concurrently with a Request for Continued Examination pursuant to 37 C.F.R. §1.114.

Amendments to the Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 14 of this paper.